

NOV 16 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/695,727
Filing Date October 28, 2003
Confirmation No. 5546
Inventor Craig M. Carpenter et al.
Assignee Micron Technology, Inc.
Group Art Unit 1783
Examiner Karla A. Moore
Attorney's Docket No. MI22-2433
Customer No. 021587
Title: Semiconductor Substrate Processing Chamber and Accessory
Attachment Interfacial Structure

RULE 312 RESPONSE

To: Mail Stop Issue Fee
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

VIA FACSIMILE

From: Mark S. Matkin (Tel. 509-824-4276; Fax 509-838-3424)
Wells St. John P.S.
801 West First Avenue, Suite 1300
Spokane, WA 99201-3828

Responsive to the Notice of Allowance dated November 8, 2006,
Applicant remarks as follows:

REMARKS

Claims 76-88, 92-103, 108-114, 116-120 and 127-129 remain in the
application.

The above-identified application has been allowed, as indicated by a
Notice of Allowance mailed November 8, 2006. On May 19, 2005, Applicant